

IN THE SPECIFICATION

Please replace the paragraph at page 39, line 26 to page 40, line 6, with the following rewritten paragraph:

In the circuit of FIG. [[31]] 32, the transistors QP31, QP32, QP33 are successively turned off and keep off, and accordingly the RC delay successively drops in the same manner as in the circuit of FIG. 23. When capacity values of the capacitors C21 to C23 are appropriately set, it is possible to realize the delay circuit whose delay time drops with the rise of the temperature.